

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Atty. Dkt. No. 5298-02501
PM98019C

Inventor(s):

Anantha R. Sethuraman
Christopher A. Seams

**Title: PLANARIZED SEMICONDUCTOR
INTERCONNECT TOPOGRAPHY
AND METHOD FOR POLISHING
A METAL LAYER TO FORM
INTERCONNECT**



INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Applicant requests consideration of the references listed on the attached Form PTO-1449.

The references listed on the Form PTO-1449 were previously cited in application serial no. 09/143,723 to which the captioned application is seeking priority under 35 U.S.C. § 120; therefore, a copy of each reference is not enclosed.

No fee is believed to be required. If a fee is required, the Commissioner is authorized to charge Conley, Rose & Tayon, P.C. Deposit Account No. 50-1505/5298-02501.

Respectfully submitted,

Kevin L. Daffer
Reg. No. 34,146
Attorney for Applicants

Conley, Rose & Tayon
P.O. Box 398
Austin, TX 78767-0398
Date: February 7, 2001